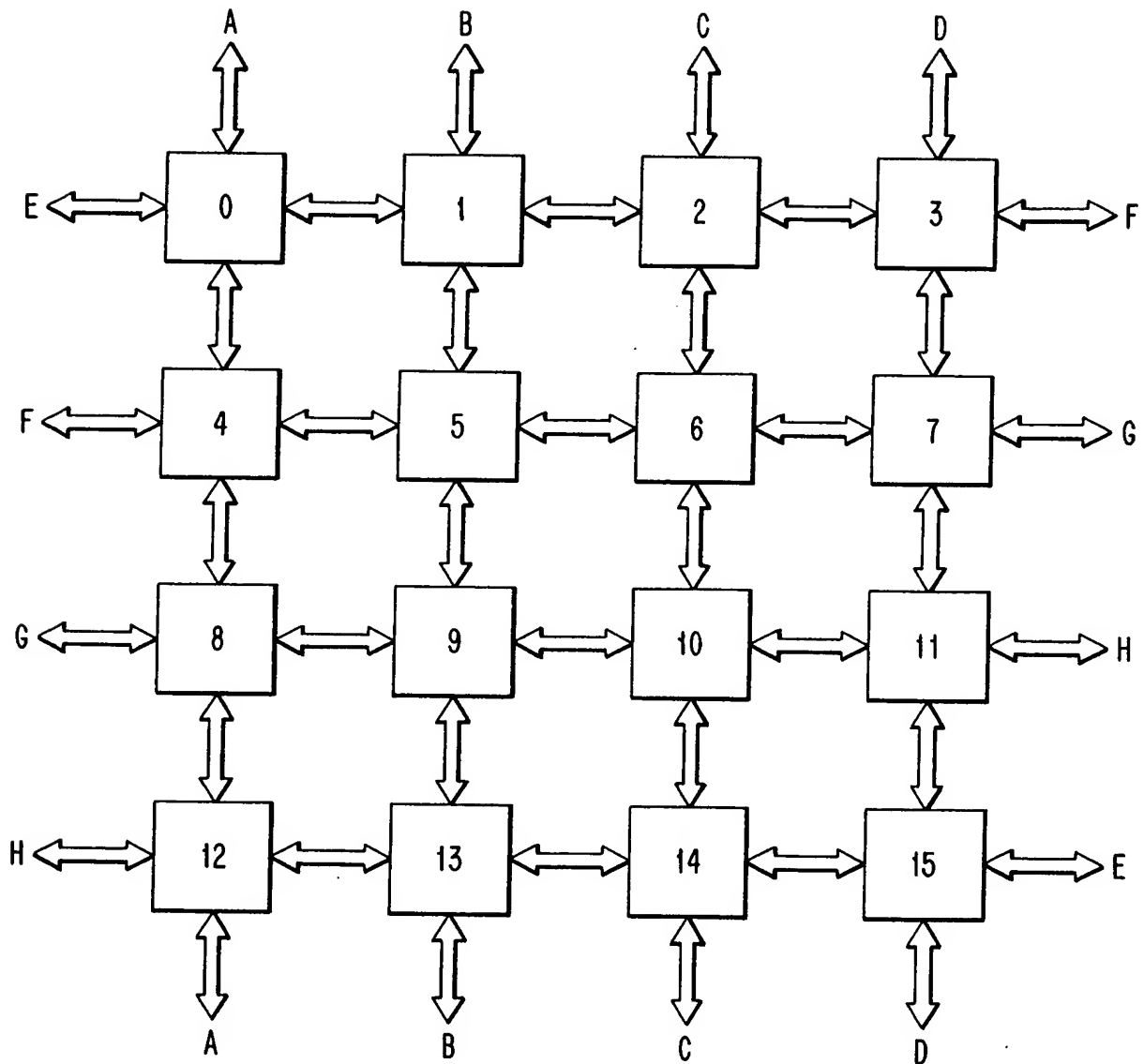
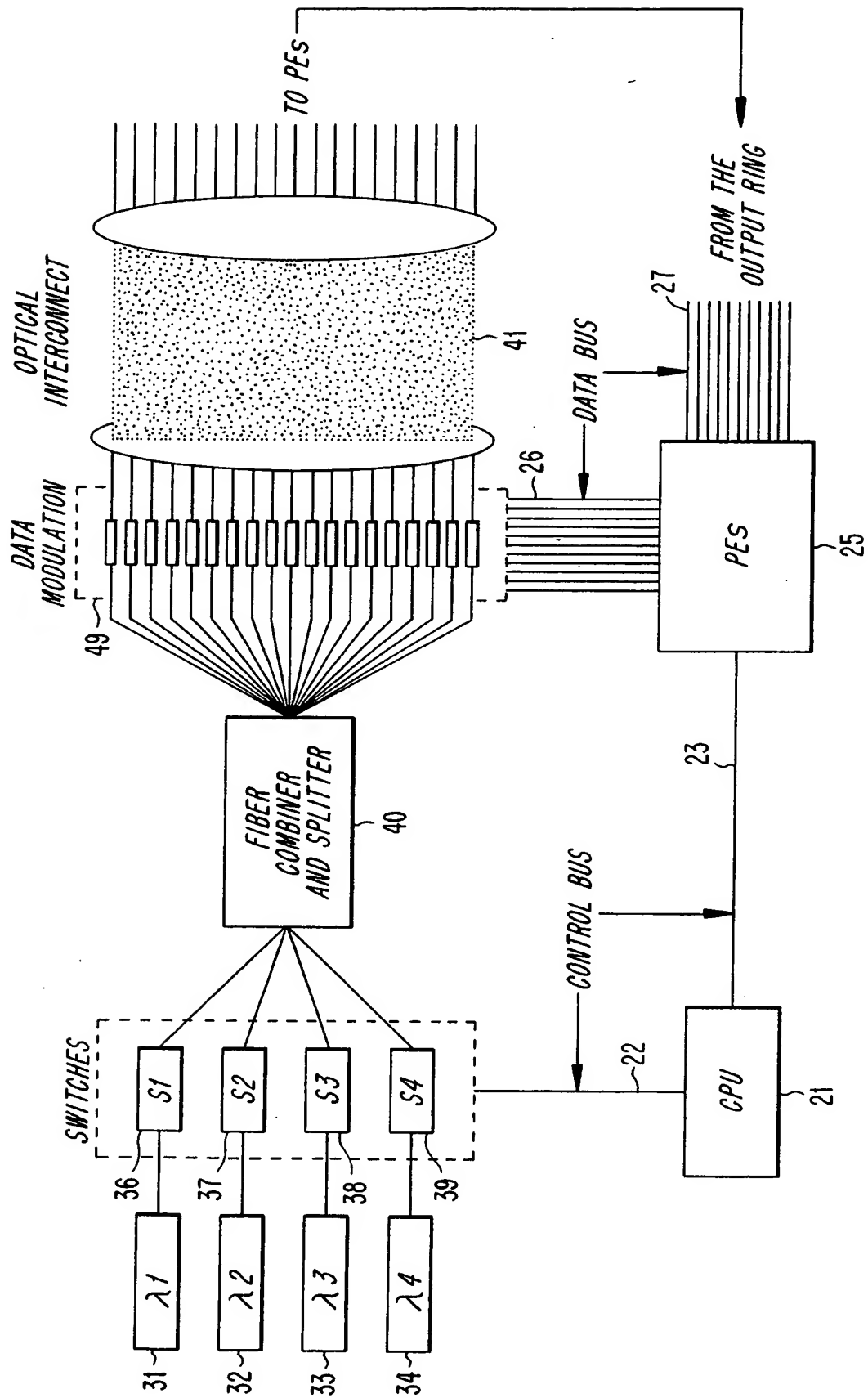


FIG. 1  
(PRIOR ART)

*FIG. 2*  
(PRIOR ART)



The diagram illustrates a multi-channel optical interconnect system. On the left, a dashed box labeled "SWITCHES" contains four vertical blocks labeled  $\lambda 1$ ,  $\lambda 2$ ,  $\lambda 3$ , and  $\lambda 4$ , with reference numerals 31, 32, 33, and 34 respectively. Each block is connected to a corresponding switch block labeled S1, S2, S3, and S4, with reference numerals 36, 37, 38, and 39 respectively. These switch blocks are connected to a central block labeled "FIBER COMBINER AND SPLITTER" (40). The output of the combiner/splitter is a fan of lines that enter a large cylindrical component labeled "OPTICAL INTERCONNECT" (41). Inside this cylinder, the lines pass through a "DATA MODULATION" section (49) and then through a "DATA BUS" section (26). The output of the optical interconnect is a bundle of lines (27) that enter a block labeled "PES" (25). The PES block is connected to a "CPU" (21) via a "CONTROL BUS" (22) and a "DATA BUS" (23). The CPU is also connected to the PES block via a "DATA BUS" (24). The PES block has an output labeled "FROM THE OUTPUT RING" (28) and a connection to "TO PES" (29).



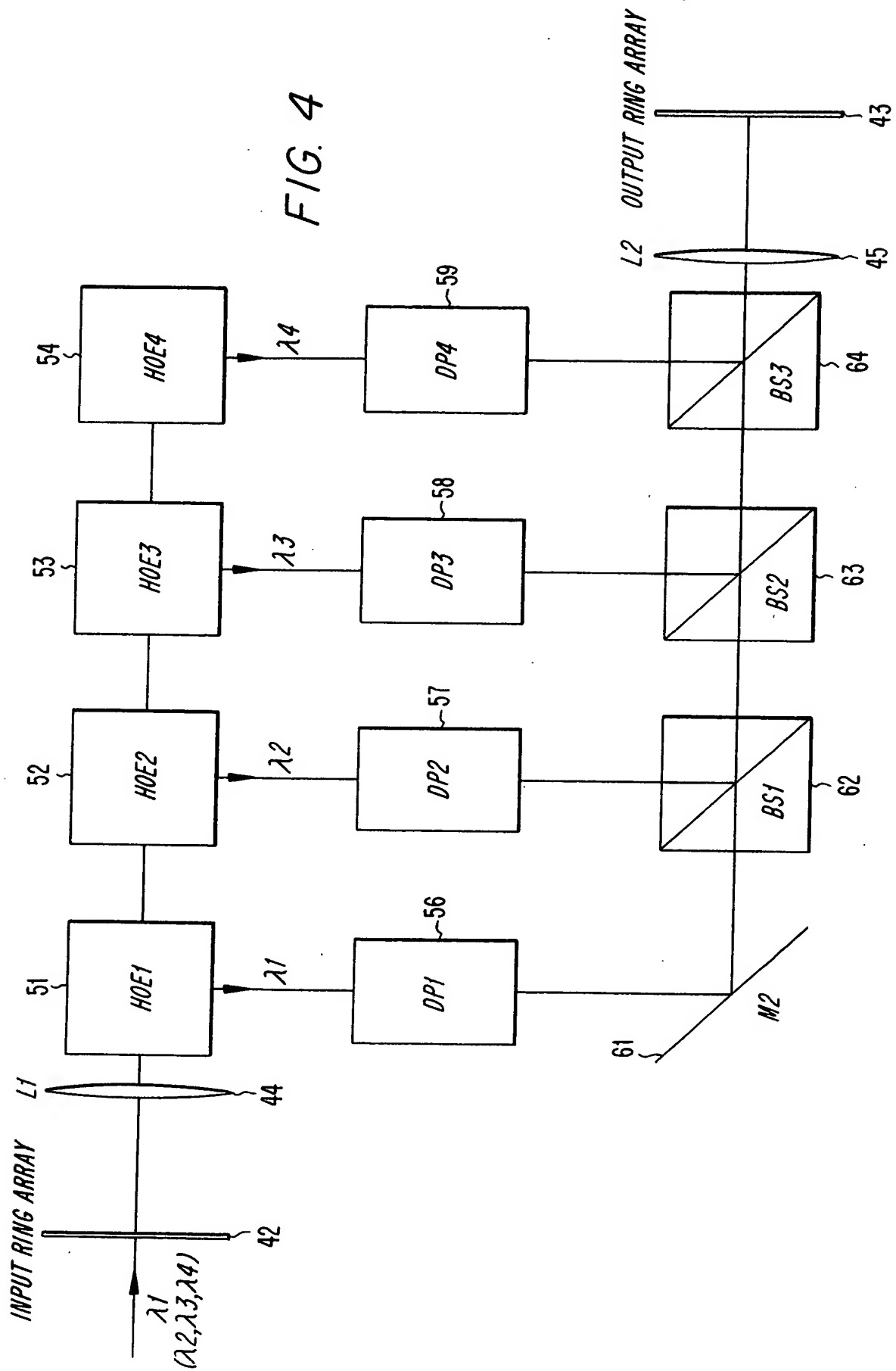


FIG. 5

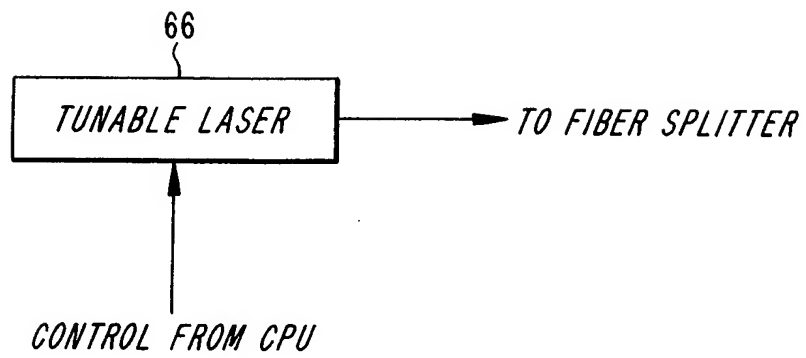


FIG. 6

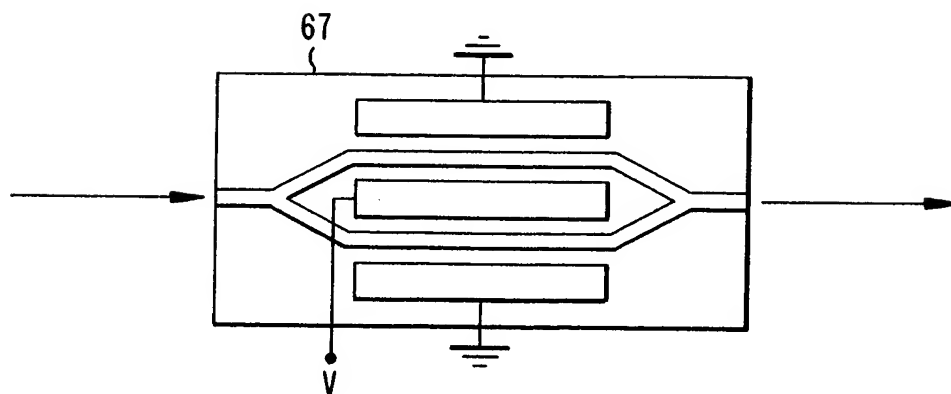


FIG. 7

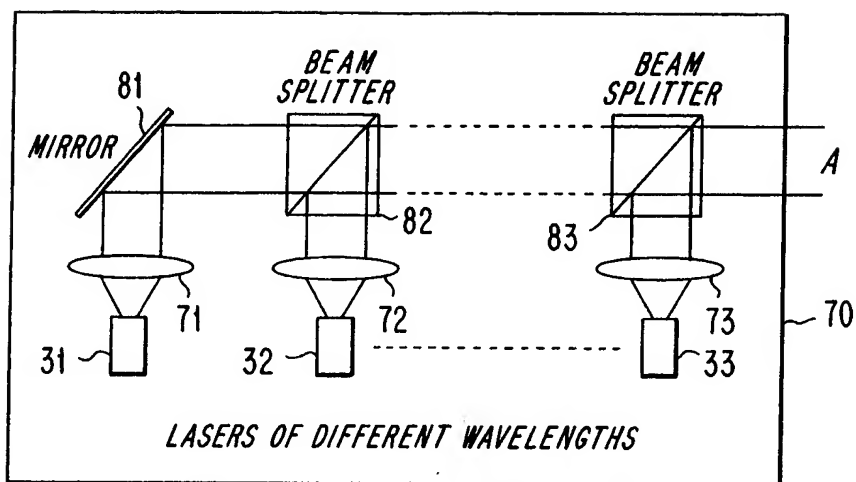


FIG. 8A

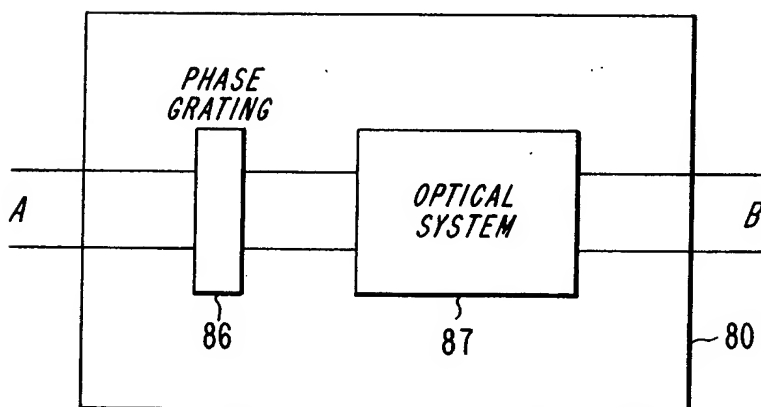


FIG. 8B

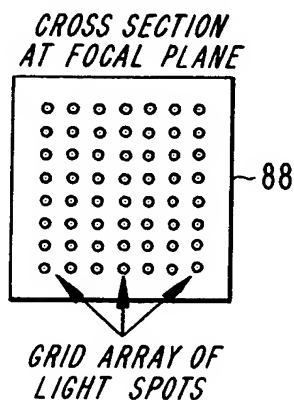


FIG. 9

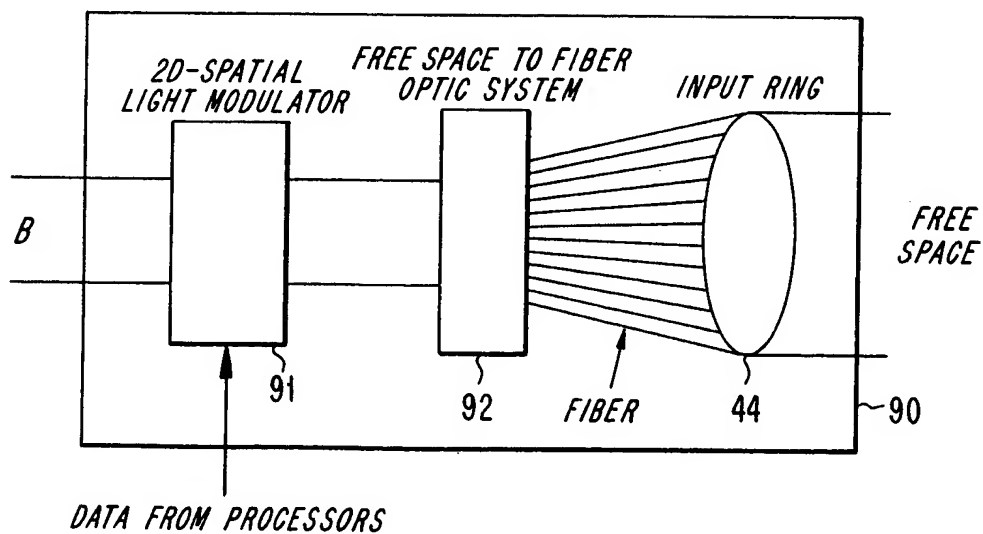


FIG. 10

